

CLAIMS

What is claimed is:

- 1 1. A moisture barrier, comprising:
2 a packaging material having a surface and at least one edge;
3 a first dielectric material layer disposed on at least a portion of said
4 packaging material surface; and
5 at least one first barrier structure disposed on said first dielectric material
6 layer proximate said packaging material edge.

- 1 2. The moisture barrier of claim 1, further including at least one
2 additional dielectric material layer disposed over said at least one first barrier
3 structure and said first dielectric material layer.

- 1 3. The moisture barrier of claim 2, further including at least one second
2 barrier structure contacting said at least one first barrier structure, wherein at least a
3 portion of said at least one second barrier structure extends through said at least one
4 additional dielectric material layer.

- 1 4. The moisture barrier of claim 1, wherein said packaging material
2 comprises an encapsulation material.

- 1 5. The moisture barrier of claim 1, wherein said packaging material
2 comprises a packaging core material.

- 1 6. The moisture barrier of claim 1, wherein said packaging material
2 comprises a heat spreader.

- 1 7. A microelectronic package, comprising:
2 a microelectronic die having an active surface and at least one side;

3 packaging material adjacent said at least one microelectronic die side,
4 wherein said packaging material including a surface substantially planar to said
5 microelectronic die active surface and including at least one edge;
6 a first dielectric material layer disposed on at least a portion of said
7 packaging material surface; and
8 at least one first barrier structure disposed on said first dielectric material
9 layer proximate said packaging material edge.

1 8. The microelectronic package of claim 7, further including at least
2 one additional dielectric material layer disposed over said at least one first barrier
3 structure and said first dielectric material layer.

1 9. The microelectronic package of claim 8, further including at least
2 one second barrier structure, wherein at least a portion of said at least one second
3 barrier structure extends through said at least one additional dielectric material layer
4 and contacts said at least one first barrier structure.

1 10. The microelectronic package of claim 7, wherein said packaging
2 material comprises an encapsulation material.

1 11. The microelectronic package of claim 7, wherein said packaging
2 material comprises a packaging core material.

1 12. The microelectronic package of claim 7, wherein said packaging
2 material comprises a heat spreader.

1 13. A microelectronic package, comprising:
2 a microelectronic die having an active surface and at least one side;
3 packaging material adjacent said at least one microelectronic die side,
4 wherein said packaging material including at least one surface substantially planar
5 to said microelectronic die active surface and including at least one edge;

6 a first dielectric material layer disposed on at least a portion of said
7 packaging material surface;
8 at least one first conductive trace disposed on said first dielectric material
9 layer with a portion of said at least one first conductive trace extending through said
10 first dielectric material layer to contact said microelectronic die active surface; and
11 at least one first barrier structure disposed on said first dielectric material
12 layer proximate said packaging material edge.

1 14. The microelectronic package of claim 13, further including at least
2 one additional dielectric material layer disposed over said at least one first barrier
3 structure, said at least one first conductive trace, and said first dielectric material
4 layer.

1 15. The microelectronic package of claim 14, further including at least
2 one second barrier structure, wherein at least a portion of said at least one second
3 barrier structure extends through said at least one additional dielectric material layer
4 to contact said at least one first barrier structure.

1 16. The microelectronic package of claim 14, further including at least
2 one second conductive trace, wherein at least a portion of said at least one second
3 conductive trace extends through said at least one additional dielectric material layer
4 to contact said at least one first conductive trace.

1 17. The microelectronic package of claim 13, wherein said packaging
2 material comprises an encapsulation material.

1 18. The microelectronic package of claim 13, wherein said packaging
2 material comprises a packaging core material.

1 19. The microelectronic package of claim 13, wherein said packaging
2 material comprises a heat spreader.

1 20. A method of fabricating a moisture barrier, comprising:
2 providing a substrate having a surface and at least one edge;
3 disposing a first dielectric material layer on at least a portion of said
4 substrate surface; and
5 forming at least one first barrier structure on said first dielectric material
6 layer proximate said at least one substrate edge.

1 21. The method of claim 20, further including disposing at least one
2 additional dielectric material layer over said at least one first barrier structure and
3 said first dielectric material layer.

1 22. The method of claim 21, further including contacting at least one
2 second barrier structure with said at least one first barrier structure, wherein at least
3 a portion of said at least one second barrier structure extends through said at least
4 one additional dielectric material layer.

1 23. The method of claim 20, wherein said forming said at least one first
2 barrier structure comprises:
3 depositing a seed layer on said first dielectric material layer;
4 patterning a resist layer on said metal seed layer to define at least one
5 elongate opening having a desired pattern for said at least one first barrier structure;
6 plating a metal on said seed layer within said at least one elongate opening;
7 removing said resist layer;
8 removing a portion of said seed layer not having said metal plated thereon.

1 24. The method of claim 23, further comprising:
2 disposing a second dielectric material layer on said first dielectric material
3 and said at least one first barrier structure;
4 forming at least one trench through said second dielectric material layer to
5 expose a portion of said at least one first barrier structure; and

6 forming at least one second barrier structure on said second dielectric
7 material layer, wherein a portion of said second barrier structure extends into said at
8 least one trench to contact said at least one first barrier structure.

1 25. A method of fabricating a microelectronic package, comprising:
2 providing at least one microelectronic die having an active surface and at
3 least one side;
4 providing a packaging material adjacent said at least one microelectronic die
5 side, wherein said packaging material provides a surface substantially planar to said
6 microelectronic die active surface;
7 disposing a first dielectric material layer on at least a portion of said
8 microelectronic die active surface and said package material surface;
9 forming at least one first via through said first dielectric material layer to
10 expose a portion of said microelectronic die active surface;
11 forming at least one first conductive trace on said first dielectric material
12 layer, wherein a portion of said first conductive trace extends into said at least one
13 first via to electrically contact said microelectronic die active surface; and
14 forming at least one first barrier structure on said first dielectric material
15 layer proximate an edge of said package material surface.

1 26. The method of claim 25, wherein said forming said at least one
2 conductive trace and said forming at least one first barrier structure comprises
3 simultaneously forming said at least one first conductive trace and said at least one
4 first barrier structure.

1 27. The method of claim 26, wherein said simultaneously forming said at
2 least one first conductive trace and at least one first barrier structure comprises:
3 depositing a seed layer on said first dielectric material layer which extends
4 into said first via;

5 patterning a resist layer on said seed layer to define at least one opening
6 having a desired pattern for said at least one first conductive trace and at least one
7 elongate opening having a desired pattern for said at least one first barrier structure;
8 plating a metal on said seed layer within said at least one opening and said at
9 least one elongate opening;
10 removing said resist layer;
11 removing a portion of said seed layer not having said metal plated thereon.

1 28. The method of claim 27, further comprising:
2 disposing a second dielectric material layer on said first dielectric material,
3 said at least one first conductive trace, and said at least one first barrier structure;
4 forming at least one second via through said second dielectric material layer
5 to expose a portion of said at least one first conductive trace;
6 forming at least one second conductive trace on said first dielectric material
7 layer which extends into said at least one second via to electrically contact said at
8 least one conductive trace;
9 forming at least one trench through said second dielectric material layer to
10 expose a portion of said at least one first barrier structure; and
11 forming at least one second barrier structure on said second dielectric
12 material layer which extends into said at least one trench to contact said at least one
13 first barrier structure.

1 29. A method of fabricating a moisture barrier, comprising:
2 disposing a first dielectric material layer on at least a portion of a substrate
3 surface, the substrate including a surface and at least one edge; and
4 forming at least one first barrier structure on said first dielectric material
5 layer proximate said at least one substrate edge.

1 30. The method of claim 29, further including disposing at least one
2 additional dielectric material layer over said at least one first barrier structure and
3 said first dielectric material layer.

1 31. The method of claim 29, further including contacting at least one
2 second barrier structure with said at least one first barrier structure, wherein at least
3 a portion of said at least one second barrier structure extends through said at least
4 one additional dielectric material layer.

1 32. The method of claim 31, further including contacting at least one
2 additional barrier structure with said at least one second barrier structure.

1 33. The method of claim 29, further including disposing from one to four
2 dielectric material layers over said at least one first barrier structure and said first
3 dielectric material layer.

1 34. The method of claim 29, further including disposing from one to
2 three additional barrier structures over said at least one first barrier structure and
3 said first dielectric material layer.